SYMPULS AACHEN

Pattern Receiver SBF 40GIG

Self synchronising, wideband Pattern Receiver with error counter. Can be used for bit error rate testing together with Pattern Generator BMG 30GIG.

TECHNICAL DATA:

Synchronisation:	1 GBit/s ... 40 GBit/s (with external clock) Frame synchronisation automatically, Phase tolerance between Data and Clock: +/- 20% of the clock periode Phase shifting: +/- 15ps by adjustable delay Synchronisation is displayed by LED.
Clock Input:	50 Ohm SMA, AC-coupled, |r| < 0.2, U_i = 0.3 ... 1.0 V_{pp}, selectable between: 1 Ghz ... 40 Ghz (Clock) and 0.5 Ghz ... 20 Ghz (Clock/2)
Pattern:	PRBS: 2^{31} - 1, 2^{23} - 1, 2^{15} - 1, 2^{7} - 1
Data Input:	NRZ or /NRZ, selectable, U_i = 0.3V_{pp} ... 1V_{pp}, 50 Ohm SMA, |r| < 0.2, manually selectable threshold
Trigger Outputs:	1. Clock/64, 1 V_{pp}, AC-coupled, 50 Ohm SMA 2. Word Frame Trigger, 0.8 V_{pp}, 50 Ohm SMA
Error Counter:	Error rate measurement (Resolution 6 digits): 1. Error/time: 10^{7}... 10^{-4} s^{-1} Gate Time: 10µs ... 10.000 s 2. Error/Bit: 10^{-3} ... 10^{-14} Gate Time: 10^{5} ... 10^{14} Bits Single or repetitive.
Mech. dim.:	19" desktop, W x H x D = 470 x 150 x 370 mm
Weight:	approx. 9Kg
Power Supply:	115V/230V/50Hz-60Hz/80VA

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